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Common Background Signals in Voltammograms of Crystalline Silicon Electrodes are Reversible Silica–Silicon Redox Chemistry at Highly Conductive Surface Sites

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ABSTRACT: The electrochemical reduction of bulk silica, due to its high electrical resistance, is of limited viability, namely requiring temperatures in the excess 850 °C. By means of electrochemical, and electrical measurements in atomic force microscopy, we demonstrate that at a buried interface, where silica has grown on highly conductive Si(110) crystal facets, the silica–silicon conversion becomes reversible at room temperature and accessible within a narrow potential window. We conclude that parasitic signals commonly observed in voltammograms of silicon electrodes originate from silica–silicon redox chemistry. While these findings do not remove the requirement of high temperature towards bulk silica electrochemical reduction, they however redefine for silicon the potential window free from parasitic signals, and as such, significantly restrict the conditions where electroanalytical methods can be applied to the study of silicon surface reactivity.

Cyclic voltammetry still reigns as the main form of electrochemical spectroscopy.¹ From electro-catalysis, to sensing and corrosion, this straightforward current–potential measurement is a widespread tool for studying redox reactions at solid–liquid interfaces.² For both metals and semiconductors,^{3–6} and for diffusive and diffusion-less systems,^{7–9} kinetic, mass transport and thermodynamic parameters of redox reactions are readily obtained from the analysis of voltammetric currents (peak positions and intensities).¹⁰

Owing to the simplicity of recording current magnitude against time – charges – chemists and surface scientists routinely turn to Coulomb values extracted from cyclic voltammograms to monitor the progress and estimate yields of surface reactions.^{9, 11–15} Even the chemisorption of just enough molecules to results in a fractional monolayer (e.g. <0.1 ng cm⁻²) generally leads to currents well above the noise of basic commercial potentiostats.^{16–18} This aspect makes voltammetry one of the most sensitive analytical techniques for studying the reactivity of surfaces.¹⁹

Silicon remains the technologically most relevant semiconductor, and research is constantly expanding the pool of reactions targeting its surface.^{20, 21} However, the power of voltammetry, when applied to silicon surface science, hinges around a correct understanding of background current signals. While for platinum, gold and carbon, all common adventitious voltammetric signals have been explained and assigned,²² recurrent parasitic signals observed for silicon electrodes remain unexplained.

Several published reports, dealing with the surface reactivity of silicon crystals, carry clear evidence of a pair of adventitious redox waves, either disregarded as artifacts, or often tentatively associated with the adsorption of a target molecule.^{9, 23–26} Herein we demonstrate how to systematically reproduce and amplify this parasitic signal. We bring evidence of this signal being the reversible

electrochemical conversion of silica to silicon, taking place at the buried silicon–silica interface in correspondence of ubiquitous highly conductive crystal defects.

Figure 1 illustrates the process of deliberately oxidizing a monolayer-coated, oxide-free,²⁷ Si(111) surface in aqueous 1.0 M HClO₄. This is an exceedingly common electrode–electrolyte system for silicon electrochemistry,^{28–30} and when we first ramped the voltage, from –0.5 V to a relatively low anodic vertex of 0.0 V, nothing unexpected appeared in the current trace (Figure 1a). Raising the anodic vertex from 0.0 to 1.0 V led again, at a first look, to a featureless voltammogram, with the only exception of a steep rise in current which is generally associated with the oxidation of either the substrate or the solvent (Figure 1b). However, a closer inspection of the trace in Figure 1b indicates the appearance of a surface-confined cathodic wave, labelled as **1** in the return segment of the first cycle (Figure 1c). This wave is coupled to a new anodic signal, evident in the third segment (**2**). These new signals increase in size, and shift anodically, upon further cycling (waves labelled as **3–5** in Figure 1c). These waves are therefore associated with anodic damaging of the electrode, that is, the appearance of silica on its surface. Figure S1 shows the changes in the XPS Si 2p narrow scan after the anodic process, revealing the appearance of a photoemission at binding energy of 102 eV, which is assigned to SiO_x species.³¹ From the above results we infer that the background reversible redox peaks, of which we are trying to define the origin, are most likely related to silicon oxides. This is surprising, since the reduction of silica requires normally very harsh conditions (high temperatures and molten salts electrolytes, *vide infra*).^{32–34} Further, these waves reflect an irreversible chemical change of the electrode, as opposed to a simple capacitive process. Data in Figure S2 show that the surface-confined redox couple formed upon the anodic excursion to 1.0 V is remarkably stable to prolonged potential cycling. Its surface density, estimated under the assumption of a one-

electron redox reaction,^{35,36} is of $(2.34 \pm 0.68) \times 10^{-12}$ mol cm^{-2} , which is equivalent to about 0.1% of the surface atoms density of an ideal Si(111) surface (15.66×10^{14} atoms cm^{-2}).

This last quantitative remark is important, since this

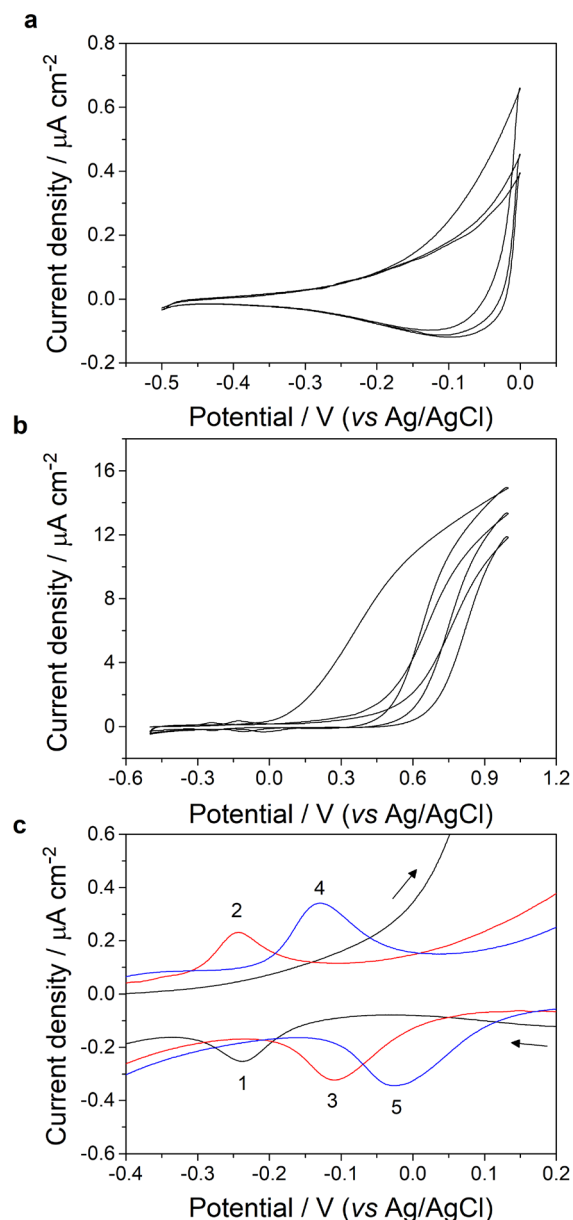


Figure 1. Cyclic voltammograms of 1,8-nonadiyne-modified Si(111) electrodes. The electrode voltage is ramped from an initial -0.5 V to an anodic vertex, set to either 0.0 V in (a), or to 1.0 V in (b). (c) Color-coded magnified views of the first three sequential voltammetric cycles (six segments) shown in (b). The voltage scan rate is 0.1 V/s, and the electrolyte is aqueous 1.0 M HClO_4 .

fractional coverage is close to the ratio, expected for a Si(111) wafer, of atoms on step-edges to atoms on surface terraces. There is in fact a strong dependence between the electrical conductivity of a silicon surface and its crystal orientation.³⁷ Recently published electrical measurements, performed with microscopic tungsten probes, have revealed that the electrical conductivity for common low-index silicon facets decrease in the order

$(110) \gg (111) > (100)$.³⁷ We have observed similar facet-dependent conductivity trends in small Cu_2O crystals.³⁸ We therefore postulate that the electrical conductivity of the silicon- SiO_x interface is enhanced at sites where silica grows on surface defects of large conductivity.³⁷ Under

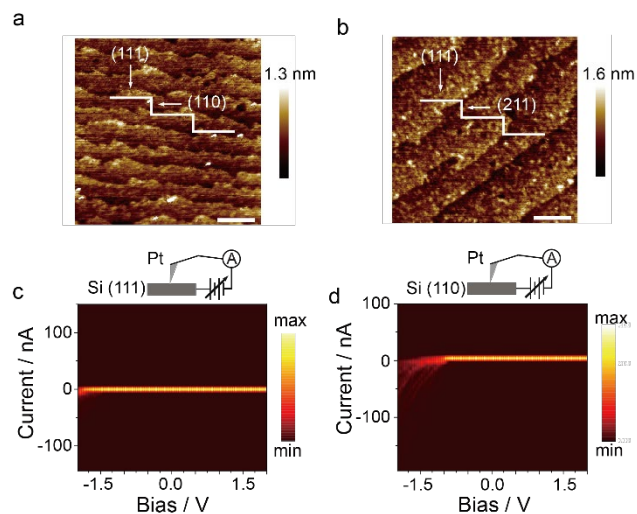


Figure 2. (a,b) Atomic force microscopy height images of monolayer-coated Si(111) wafers. Topography images were acquired paying attention to ensure a parallel alignment between the major flat, indicating the $[110]$ direction, and the x-direction of the AFM raster scan. Steps between terraces are either roughly parallel to the sample major flat (a), or oriented 30° away from it (b). Scale bars in (a) and (b) are 400 and 220 nm, respectively. (c,d) Heat maps of current-potential (I - V) data for platinum-silicon junctions acquired by conductive AFM on Si(111) and Si(110) surfaces. I - V curves are sampled at 100 evenly spaced points and are recorded at a constant force of 2.5 μN with a 100 nA/V sensitivity. The sample-to-tip bias routing is such that forward currents appear in the negative quadrant, that is, when the n-type silicon is biased negative with respect to the platinum AFM tip

these circumstances the silica-silicon redox couple may become reversible within a moderate potential window, therefore explaining the origin of the parasitic signals ubiquitous to oxidized silicon electrodes (Figure 1).

We therefore investigated the existence of highly conductive defects on nominal single-crystal silicon wafers. Atomically flat electrodes are an idealization, and well-prepared and nominally flat semiconductor electrodes are a continuous of flat terraces separated by small vertical steps.^{5,39} Atomic force microscopy (AFM) topography data in Figure 2a and 2b indicate that on Si(111) electrodes – a crystal cut commonly used by electrochemists and surface scientists^{40,41} – these vertical steps tend to have preferred orientations. Commercial Si(111) wafers have a major flat indicating the $[110]$ direction (a stereographic view of a Si(111) wafer is in Figure S3). Surface topography data obtained by AFM reveal that these vertical steps form angles of either approximately 0° or 30° with respect to the direction of the major flat (Figure 2a and 2b). Steps separating Si(111) terraces are therefore principally exposing (110) and (211) planes. An analysis of step heights and terrace widths in the AFM topography images (e.g. Figure 2a) revealed that even in high quality samples, such as those in Figure 2, the surface area ratio of Si(110) to

Si(111) is 0.19 ± 0.05 to 100. This percentage is in striking accordance with the 0.1% surface coverage, estimated above by cyclic voltammetry, for the adventitious redox couple appearing upon oxidation to the silicon electrode (Figure 1). Representative AFM height profiles, acquired along a direction normal to the predominant step direction, and used to estimate the area ratio of (110) and (211) facets to (111) terraces in nominal Si(111) wafers, are in Figure S4.

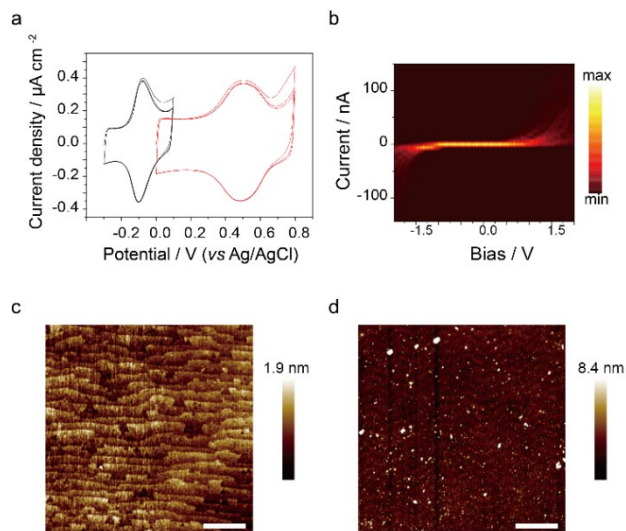


Figure 3. (a) Cyclic voltammograms of Si(111) electrodes indicating an anodic shift with progressive potential cycling (three cycles, black curves; 100 cycles, red curves) of the apparent formal potential of the silica/silicon redox couple. The scan rate is 0.1 V/s, and the electrolyte is 1.0 M HClO₄. (b) I–V curves (AFM) of platinum–silicon junctions recorded on anodically damaged Si(111) electrodes, showing a significant increase in the leakage current (positive quadrant) compared to fresh samples (Figure 2c). (c,d) Changes to surface topography before (c) and after (d) three potential sweeps between –0.5 V and 1.0 V (0.1 V/s, 1.0 M HClO₄). Scale bars in (c) and (d) are 1 μm .

It was therefore relevant to test the relative conductivity of Si(110) and Si(111) facets. Current–potential data (I–V hereafter), acquired by conductive AFM (Figure 2c and 2d, and Figures S5 and S6), indicate that the conductivity of Si(110) wafers is significantly larger than that of Si(111). For example, at a forward bias of –1.5 V, the current of platinum–Si(110) junctions is on average 21 times larger (–48 nA versus only –2.20 nA) than that of junctions made to Si(111). Silicon has no conductive bulk oxide phases,³² and the electro-reduction of silica has only been demonstrated in CaCl₂ melts.^{34,42} By ensuring good mobility of O²⁻ anions the high-temperature melt aids the de-oxidation of SiO₂.^{32, 42} However, under an electric field, oxygenated anions (O²⁻ and OH⁻) are known to migrate across a thin silica layer, even at room temperature.⁴³⁻⁴⁵ Further, the growth of silica over an oxide-free substrate introduces energy levels in the band gap,⁴⁶ demonstrated indirectly by a sharp drop of the anodization voltage observed as soon as silica adlayers form over a silicon electrode.⁴⁶⁻⁴⁹

We therefore propose that despite the low electrical conductivity of bulk silica,^{32, 34, 50} its electrochemical

reduction at the silica–silicon interface becomes apparently reversible at highly-conductive facets, and manifests in both aqueous (Figure 1 and Figure S7a–c) as well as non-aqueous electrolytes (Figure S7d–e). It has been suggested that the mobile species migrating across the thickening oxide film are mainly anions.⁵¹ Water molecules enter the first layers of the oxide and dissociate into ionic species, which then migrate toward the silicon/oxide interface under the effect of the electrical field in the oxide.^{43, 46} The apparent formal potential of this parasitic redox couple

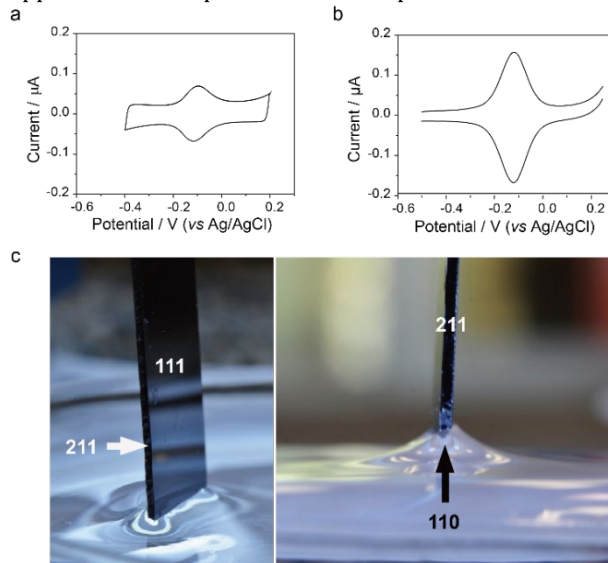


Figure 4. (a,b) Cyclic voltammograms of anodically damaged Si(111) and Si(110). The surface coverage of the redox background signal observed on Si(110) is 12.73×10^{-12} mol cm⁻², which is 6.9 times larger than on Si(111). The geometric area ratio of the two electrodes is approximately 1.9 ((111) to (110), estimated by capacitance measurements). Voltammograms were recorded at a scan rate of 0.1 V/s, in aqueous 1.0 M HClO₄, after three potential sweeps (0.1 V/s) between –0.5 V and 1.0 V. (c) Optical images of the hanging-meniscus configuration used to wet the (110) facet in a three-electrode experiment used to record the voltammograms in (b). A graphical depiction of the (211) and (110) crystal directions in commercial Si(111) wafer is in Figure S13.

shifts anodically with progressive oxidation (Figure 3a), which reflects a similar shift for the electrode open circuit potential. These shifts are caused by an increase, with oxidation, of the dark leakage current.^{5,6} I–V curves shown in Figure 3b (and in Figure S8) confirm a substantial increase in leakage current upon initial substrate oxidation. Our oxidation-induced redox background couple is therefore in series with a “leaky” silica-rich diode. As expected, the sharp and regular terraced structure of the etched and monolayer-coated Si(111) surface (Figure 3c) disappears after oxidation: the highly conductive (110) and (211) step edges have been covered by an oxide layer (Figure 3d, Figure S9).

To further confirm that the surprising silica/silicon redox reversibility arises from the silica adlayer sitting on highly conductive silicon sites, we performed electrochemical experiments on macroscopic samples cleaved to expose mainly (110) planes. This was done by cutting a Si(111) wafer along a direction parallel to the supplier-marked lap (Figure 4c). The wafer was then etched and chemically

passivated, before dipping it in the electrolyte so to wet only the (110) face. As shown in Figure 4, voltammograms acquired on anodically damaged Si(110) leads to redox waves significantly larger (ca. 6.9 fold) than on Si(111).

We note that bias-driven adsorption of ions,¹⁹ as a potential contributor to the parasitic redox signal, was ruled out (Figure S7). Furthermore, surface coverages and peak positions do not change with pH, indicating that the new couple formed after anodic damaging of the surface is the result of a charge transfer not coupled to proton transfer (Figure S10). Controls with amorphous silicon,³⁸ as well as with non-silicon conductors (Figure S11), indicated no appearance of redox signals after an anodic over-oxidation process, reinforcing that this new surface-confined redox couple relates to the crystalline structure of the silicon surface.

In conclusion, we have explored the origin of a recurrent cyclic voltammetry background signal found in a widespread semiconductor laboratory system – Si(111) electrodes. An oxidative damage, deliberate or not, of common single crystal silicon Si(111) and Si(100) electrodes (Figure S12) leads to the appearance of a specific set of voltammetric waves. This current signal is the reversible silica/silicon redox process, occurring on highly conductive steps separating (111) terraces. This finding has immediate implications in the electrochemistry, and surface chemistry, of silicon, effectively restricting the potential window that is free from parasitic signals and therefore permitting the study of surface reactions by means of electroanalytical methods.

ASSOCIATED CONTENT

Supporting Information

Experimental section, stereographic projections, additional AFM (conductive and topography), x-ray photoelectron spectroscopy, and cyclic voltammetry data. This material is available free of charge via the Internet at <http://pubs.acs.org>

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Notes

The authors declare no competing financial interest.

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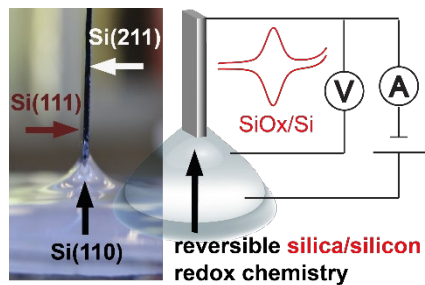
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SYNOPSIS TOC



Facet-resolved electrochemistry, and conductive atomic force microscopy, reveal that traces of silica grown on highly conductive Si(110) crystal facets leads to reversible electrochemical silica-to-silicon reduction. Minor oxidative damaging of nominal single crystal electrodes causes parasitic signals whose origin is now explained as the room-temperature reversible silica–silicon redox conversion.
